

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S64	5	717/135.ccls. dsp fpga (partition\$4 or co or schedul\$6)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/17 16:15
S63	4	717/135.ccls. dsp fpga (partition\$4 or co)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/17 16:12
S62	7	717/135.ccls. dsp fpga	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/17 16:12
S61	176	717/135.ccls. simulat\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/17 16:07
S60	10	((radar or computing or computation\$3 or numeric\$2) same simulat\$4) fpga vhdl dsp ((partition \$4 or schedul\$6 or co or simulat\$4)). clm. (mip\$1 or (soft adj \$5path\$2) or (hard adj \$5path \$2)) ((partition\$4 or schedul\$6 or co) same (fpga or dsp))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/17 16:07
S59	16	((radar or comput \$8 or numeric\$2) near2 simulat\$4) fpga vhdl dsp ((partition\$4 or schedul\$6 or co)). clm. (data\$1path \$2 or (data adj path \$2) or (soft adj \$5path\$2) or (hard adj \$5path\$2))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/17 15:53

S58	25	((radar or comput \$8 or numeric\$2) near2 simulat\$4) fpga vhdl dsp ((partition\$4 or schedul\$6 or co)). clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/17 15:49
S57	18	((radar or comput \$8 or numeric\$2) near2 simulat\$4) fpga vhdl dsp ((partition\$4 or schedul\$6)).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/17 15:49
S56	102	((radar or comput \$8 or numeric\$2) near2 simulat\$4) fpga vhdl dsp	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/17 15:48
S55	47	vhdl dsp fpga (simulat\$6 (schedul \$8) (shar\$4 or co)) (partition\$4 or divid \$4 or rout\$4) numeric\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/16 18:11
S54	25	vhdl dsp fpga (simulat\$6 (schedul \$8) (shar\$4 or co)) (partition\$4 or divid \$4 or rout\$4) numeric	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/16 18:11
S53	129	vhdl dsp fpga (simulat\$6 (schedul \$8) (shar\$4 or co)) (partition\$4 or divid \$4 or rout\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/16 18:10
S52	132	vhdl dsp fpga (simulat\$6 (schedul \$8) (shar\$4 or co))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/16 18:10
S51	150	vhdl dsp fpga (simulat\$6 (schedul \$8))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/16 18:10
S50	702	vhdl dsp fpga	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/16 18:09

S49	4	(numeric simulat\$6 schemul\$6) (fpga same dsp same (shar\$4 or co))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/16 17:47
S45	2	10/182043	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/16 17:43
S44	1	10/182222	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/16 17:42
S43	19	(schemul\$8 simulat \$8 numeric rout\$4) (fpga dsp) (bit adj2 file)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/16 17:38
S42	16	(schemul\$8 simulat \$8 numeric rout\$4) (fpga dsp) (bit adj2 file)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/16 17:36
S41	23	(schemul\$8 simulat \$8 numeric rout\$4) (fpga dsp) (bit near3 file)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/16 17:35
S40	50	(schemul\$8 simulat \$8 numeric rout\$4) (fpga dsp) (bit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/16 17:34
S39	54	(schemul\$8 simulat \$8 numeric rout\$4) (fpga dsp)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/16 17:34
S38	847	(schemul\$8 simulat \$8) (fpga dsp)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/16 17:34
S28	9	((calculat\$4 or simulat\$3) same (fpga or dsp)) (schemul\$6 same fpga same dsp) numerical	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/12 16:10

S27	6	((calculat\$4 or simulat\$3) same (fpga or dsp)) (schedule\$6 same fpga same dsp) numerical	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/12 16:09
S26	5	((calculat\$4 or simulat\$3) with (fpga or dsp)) (schedul\$6 same (fpga dsp)) numerical (dsp same fpga) (rout\$3 same (fpga or dsp))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/12 15:46
S25	6	((calculat\$4 or simulat\$3) with (fpga or dsp)) (schedul\$6 same (fpga dsp)) numerical (dsp same fpga) rout\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/12 15:46
S24	7	((calculat\$4 or simulat\$3) with (fpga or dsp)) (schedul\$6 same (fpga dsp)) numerical (dsp same fpga)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/12 14:46
S23	7	((calculat\$4 or simulat\$3) with (fpga or dsp)) (schedul\$6 same (fpga dsp) numerical (dsp same fpga))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/12 14:23
S22	18	((calculat\$4 or simulat\$3) same (fpga or dsp)) (fpga dsp) numerical ((partition\$4 or rout\$3 or co) same (dsp fpga))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/12 14:12
S21	161	((calculat\$4 or simulat\$3) same (fpga or dsp)) (fpga dsp) ((partition\$4 or rout\$3 or co) same (dsp fpga))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/12 14:09

S9	48	(fpga dsp) ((numerical or partition\$4) near8 (calculat\$4 or simulat\$3)) partition\$4 simulat \$4 numerical	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/08 17:33
S8	32	(fpga dsp) (numerical near8 (calculat\$4 or simulat\$3)) partition\$4 simulat \$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/08 17:32
S7	43	(fpga dsp) (numerical near8 (calculat\$4 or simulat\$3)) partition\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/08 17:32
S6	18	(fpga dsp) (numerical near8 (calculat\$4 simulat \$3)) partition\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/08 17:31
S5	26	(fpga dsp) (numerical near8 (calculat\$4 simulat \$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/08 17:31
S4	465	(fpga dsp) (numerical (calculat \$4 simulat\$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/05/08 17:31

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